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ATTORNEY DOCKET NO. 09108.0003
CUSTOMER NO. 22,852



Mail Stop Patent Application

**Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450**

New U.S. Patent Application
Title: SEMICONDUCTOR DEVICE AND METHOD OF FABRICATING
THE SAME
Inventor's Name and Residence:

Kazutaka AKIYAMA
Matsudo-shi, Chiba, Japan

Sir:

We enclose the following papers for filing in the United States Patent and Trademark Office in connection with the above patent application.

1. Application- 16 pages, including 2 independent claims and 20 claims total.
2. Drawings- 5 sheets of formal drawings containing 10 figures. (Figures 1-10).
3. The filing fee is calculated as follows:

Basic Application Filing Fee					\$750	\$ \$750.00
	Number of Claims		Basic	Extra Claims		
Total Claims	20	-	20		x \$18	
Independent Claims	2	-	3		x \$84	
<input type="checkbox"/> Presentation of Multiple Dep. Claim(s)					+\$280	
Subtotal						\$ 750.00
Reduction by 1/2 if small entity						-
TOTAL APPLICATION FILING FEE						\$ 750.00

4. A check for \$750.00 is enclosed for the filing fee.

This application is being filed under the provisions of 37 C.F.R. § 1.53(f). Applicant awaits notification from the Patent and Trademark Office of the time set for filing the Declaration.

Applicant claims the right to priority based on Japanese Application No. 2003-194419, filed July 9, 2003.

Please address all correspondence with respect to this application to:

Finnegan, Henderson, Farabow,
Garrett & Dunner, L.L.P.
1300 I Street, N.W.
Washington, D.C. 20005-3315

Please accord this application an application number and filing date.

The Commissioner is hereby authorized to charge any additional filing fees due and any other fees due under 37 C.F.R. § 1.16 or §1.17 during the pendency of this application to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
GARRETT & DUNNER, L.L.P.

By: 

Richard V. Burgujian
Reg. No. 31,744

RVB/FPD/gah
Enclosures

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